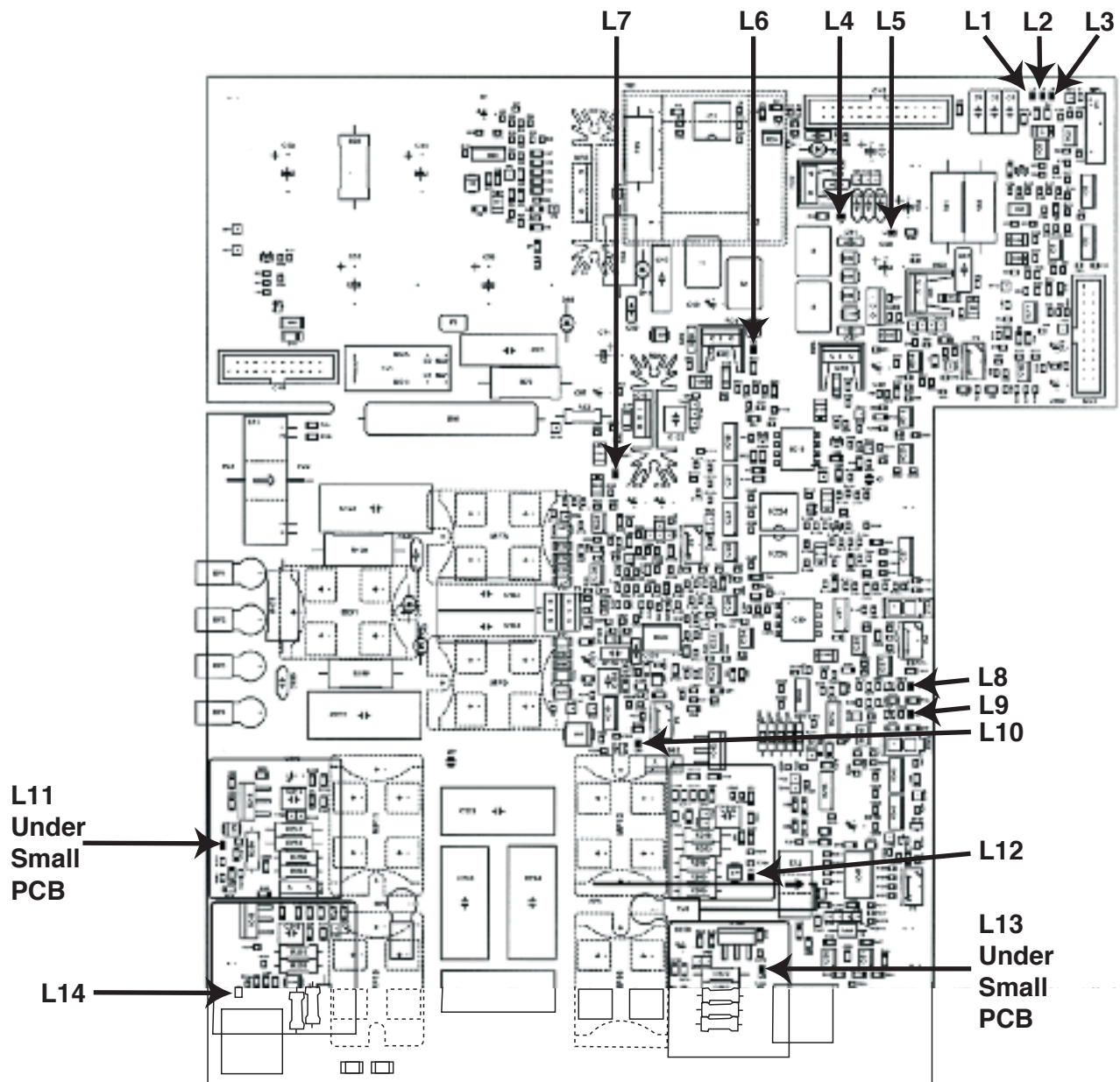


## INTERNAL DIAGNOSTICS

FIGURE F.2



INTERNAL DIAGNOSTICS (*Continued*)

LED	Definition	Normal Status Conditions
L1	-15VDC supply by 15.14.316 for 15.14.316 logic part	ON [Green]
L2	+15VDC supply by 15.14.316 for 15.14.316 logic part	ON [Green]
L3	+5VDC supply by 15.14.316 for 15.14.316 logic part	ON [Green]
L4	+24VDC supply by 15.14.316 for power front panel	ON [Green]
L5	+48VDC supply by 15.14.316 for power front panel	ON [Green]
L6	-15VDC supply by 15.14.316 for 15.14.316 UPFR part	ON [Green]
L7	+15VDC supply by 15.14.316 for 15.14.316 UPFR part	ON [Green]
L8	Output power enabled (See <i>“pot signal”</i> & <b>LED “Output”</b> )	STICK: ON [Green] TIG: ON (torch trigger pulled)
L9	Thermal Alarm	OFF = OK ON = ALARM [Red]
L10	UPFR on	Brightness proportional with actual output current.
L11	Gate MOS 11 active (output power enabled)	STICK: ON [Green] TIG: ON (torch trigger pulled)
L12	Gate MOS 12 active (output power enabled)	STICK: ON [Green] TIG: ON (torch trigger pulled)
L13	Gate MOS 16 active (output power enabled)	STICK: ON [Green] TIG: ON (torch trigger pulled)
L14	Gate MOS 15 active (output power enabled)	STICK: ON [Green] TIG: ON (torch trigger pulled)